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# Coupled electron-phonon transport and heat transfer pathways in graphene nanostructures



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#### ABSTRACT

A hybrid Boltzmann transport equation model coupled with a two-temperature model is developed to simulate the heat dissipation in single layer graphene (SLG) suspended or supported on SiO<sub>2</sub> and with/ without Cu contacts. This hybrid model includes the phonon transport in SLG, electron-phonon interaction (EPI) in SLG, and phonon/electron transmission at the interface between SLG and its surroundings. The EPI strength between electrons and various phonon modes is obtained at different temperatures using first-principle calculations. Simulations are performed for SLG with different graphene lengths, contact configurations and heating powers. For the SLG suspended on SiO<sub>2</sub>, adding Cu contact at ends can remarkably improve the heat dissipation and lower the maximum temperature in SLG, especially at small lengths of SLG. The SLG/Cu interface can still be the major heat dissipation pathway when its contact area is similar with that of SLG/SiO<sub>2</sub> interface, which can be attributed to the higher thermal conductivity of Cu. For the SLG fully supported on SiO<sub>2</sub>, adding Cu contact can effectively lower the temperature in SLG of small length, but the dominating heat dissipation pathway will be shifted from SLG/Cu interface to SLG/SiO<sub>2</sub> interface as the SLG length increases.

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#### 1. Introduction

Due to the exceptional properties such as high electron mobility, high thermal conductivity and high Young's modulus, graphene has been widely investigated for many applications. The intrinsic thermal conductivity of graphene has been reported as high as 2000–5300 W/mK at room temperature by many experimental [1–3] and numerical studies [4–7]. In electronic devices for high-field applications, the use of graphene can significantly increase the current carrying capacity. But the current-induced breakdown of device is still thermally activated, and the effective heat dissipation in graphene is critical for the thermal management [8–10]. The dominant heat carriers in graphene are phonons, and their interaction with electrons plays an important role in the heat dissipation in graphene based devices.

The heat transfer from graphene nanostructure to its surrounding may have unique characteristics which should be taken into account in the models employed for the thermal transport study. Firstly, due to the 2D structure of graphene, the phonon

\* Corresponding author. E-mail address: liangchen@mail.xjtu.edu.cn (L. Chen). transport exhibits strong mode dependent behavior which is due to the large difference in the mean free path (MFP) of the acoustic and optical phonons. In the pristine single layer graphene (SLG), the out-of-plane acoustic (ZA) modes have MFP in micrometers [5,11], the longitudinal and transverse acoustic modes have relatively smaller MFP, but the optical modes have one to two orders of magnitude smaller MFP than the acoustic modes [12,13]. A model that accounts for the contributions of different modes is needed for the investigation of thermal transport in graphene. Secondly, the thermal transport in graphene devices involves different carriers such as electrons and phonons, and the energy exchange between them can have significant impact on the heat transfer [9]. Graphene makes contact with complex surroundings including dielectrics and metals in its electronic devices. Because of the different heat carriers involved and different interface properties with different contacts, exploration of the interfacial thermal transport mechanism gets complicated. Phonons are the major heat carriers at the graphene and dielectric substrate interface [14], while both phonons and electrons as well as their interactions are important at the graphene and metal-electrode interface [15–18]. Because of the long phonon MFP but small size of graphene considered, the boundary scattering can suppress the thermal conductivity even at



room temperature [19,20]. The size effects and boundary scattering should be carefully considered for graphene with different phonon modes of various MFPs [21].

Raman technique [1,22] is widely used for the characterization of structural, electrical, thermal and optical properties of graphene, such as the measurement of number of graphene layers [23], the stacking order of multilayer graphene (MLG) [24,25], the crystallographic orientation of graphene [26], the thermal conductivity [1,22], the effect of strain [27], and doping on properties [28]. During Raman measurements, the energy is transferred to electrons via inelastic scattering of photons at femtosecond time scale. The following energy relaxation involves electron-electron collisions at femtoseconds scale and electron-phonon scattering at picoseconds scale [29,30]. The heat absorbed by electrons is transferred to phonons via electron-phonon interactions (EPI), followed by the phonon-dominated thermal transport in graphene and heat dissipation to its surroundings such as substrate and metal contacts. A detailed study of the energy exchange between electrons and phonons can benefit the interpretation of the related experimental measurements.

Understanding of the heat dissipation in graphene and from graphene to its surroundings (e.g., metal and  $SiO_2$ ) is critical for the efficient thermal management of graphene nano-electronics and the interpretation of experimental measurements such as by using Raman technique. In the previous studies, various models based on Boltzmann transport equation (BTE) have been developed to study the thermal transport in SLG and MLG. The studies [5,11,31] include both the normal and Umklapp scattering process in the BTE model which demonstrated that the ZA modes could dominate the thermal transport in suspended SLG. The dominant contribution from ZA modes can be attributed to the large density of flexural phonons associated with the quadratic ZA branch dispersion, and the restricted phase space for the ZA phonon scattering. The breaking of the reflection symmetry of the two-dimensional graphene can significantly suppress the ZA contribution [5]. However, several studies indicate the contribution from longitudinal acoustic (LA) and transverse acoustic (TA) could dominate the thermal conductivity of SLG. According to recent BTE [5,11,31] simulations, the significant reduction of thermal conductivity of supported SLG or MLG can be explained by the suppression of acoustic modes due to the SLG-substrate interaction or the interlayer interaction.

Although there are many BTE studies on the thermal transport of graphene, only few study has focused on the thermal transport in the graphene nanostructures considering the effects of all contacts in its electronic devices [5,11,32]. The computational cost to solve the BTE model depends on the methodology to describe phonon scattering and the system dimension. For SLG and MLG, simulations based on the detailed BTE models can be performed in a computational domain up to a size of micrometers to millimeters. However, simplifications and assumptions have to be made in order to solve the thermal transport problem in a configuration including contacts. Hybrid BTE models which combine BTE with Fourier conduction equation have been used in the previous studies on nano-electronic devices such as GaN transistors [33,34]. In the hybrid BTE, the thermal transport by the optical band or away from the ballistic transport region is usually described by the Fourier conduction equation which can help to significantly reduce the computational cost and allow the simulation at device scale. For graphene based nano-scale devices, the hybrid BTE model should appropriately consider the phonon transport in graphene, EPI in graphene, and phonon/electron transmission at the interface between graphene and its surroundings.

In this study, a computational model based on the Boltzmann transport equations is developed to study the coupled electronphonon transport in SLG supported on SiO<sub>2</sub> substrate and with metal contacts at ends. The heat dissipation through SLG/substrate and SLG/metal interfaces are quantified, and their dependence on SLG length and heating power is demonstrated. SLG/metal interface is found to be an important heat dissipation pathway at small SLG length due to the strong non-equilibrium between electrons and phonons. The findings of this study provide insights into the electron-phonon coupled transport mechanism and helps to understand the heat transfer pathways in graphene-based nanoelectronics.

#### 2. Methodology

In order to take into account the multiphysics characteristics of the energy transport in graphene, a computational model is developed using a diffusive equation for electrons and non-gray Boltzmann transport equations for phonons. The twotemperature model (TTM) proposed by Anisimov et al. [35] is employed to describe the electron-phonon coupled transport in metal. The TTM consists of diffusion and source terms, and the electron-phonon coupling factor is used in the source term to represent the rate of the energy exchange between electrons and lattice [36]. The calculation of the electron-phonon coupling factor will be discussed in the following section. The non-gray BTE model used for the phonon transport in SLG considers all phonon modes, but usually employs the single-mode relaxation time (SMRT) approximation [37,38]. Moreover, the BTE-SMRT approach can include the mode dependencies of both the phonon transport and EPI. The model requires several important parameters including mode-dependent phonon lifetime, EPI parameter, electron transmission, and phonon transmission. This section will briefly introduce the hybrid model as well as the methodology to determine these parameters. A complete description of the model parameters



**Fig. 1.** Schematic of the different configurations considered for the simulations (a) System 1: SLG suspended on SiO<sub>2</sub>; (b) System II: SLG suspended on SiO<sub>2</sub> with Cu contacts; (c) System III: SLG supported on SiO<sub>2</sub> substrate; (d) System IV: SLG supported on SiO<sub>2</sub> substrate with Cu contacts. *L* denotes the half length of SLG which varies from 24.5 nm–490 nm in current work. *L*<sub>C</sub> denotes the contact length (9.8 nm) with Cu in (b) and (d), and SiO<sub>2</sub> in (a) and (b). (A colour version of this figure can be viewed online.)

and implementation can be found in the supplementary document.

Fig. 1 shows the different configurations considered in the present work for thermal transport study: System I consists of SLG suspended on SiO<sub>2</sub>, System II consists of SLG suspended on SiO<sub>2</sub> with Cu contacts, System III consists of SLG supported on SiO<sub>2</sub> substrate, and System IV consists of SLG supported on SiO<sub>2</sub> substrate with Cu contacts. The heat source is applied in SLG in the center of the device. Considering the symmetry of the structures, only right half of the device is simulated for all cases, as shown in Fig. 1 (a).

In Fig. 1 (a), (b) and (d), the contact length between SLG and Cu or SiO<sub>2</sub> is set at 9.8 nm. The length of the simulated structures (half of the device length as shown in Fig. 1) varies from 24.5 nm to 490 nm. The heat absorbed by electrons in SLG is assumed to follow Gaussian distribution in *x* direction, *e.g.*,  $P(x) = I/(\sqrt{2\pi}\sigma) \cdot e^{-x^2/\sigma^2}$ . In electronic devices, the hot spots have sizes ranging from several nanometers to few microns depending on the channel length and strength of electric field [39,40]. For the short channel devices, decreasing dimensions lead to nanometer-scale hot spots [39,40]. In this study, the domain size is from 24.5 nm to 490 nm, and a heating region width ( $\sigma$ ) of 4.9 nm (10% of largest size considered) is used in the simulations. A width of 424.35 nm is used to calculate the power density. The intensity I from  $2 \times 10^{-5}$  W to  $10^{-4}$  W is selected so that the lattice temperature in the simulations falls in the range of interests (<600 K) for the study of hot spots in nanoelectronics [41]. The corresponding power density ranges from 9.6  $\times$  10<sup>8</sup> W/m<sup>2</sup> ~ 4.8  $\times$  10<sup>10</sup> W/m<sup>2</sup> which is 1–2 orders of magnitude higher than the average value (~1.4  $\times$  10  $^8$  W/m²) over the entire device [42].

#### 2.1. Hybrid BTE model and electron-phonon interaction in graphene

In pristine graphene, the electron mean free path can be on order of micrometer [43,44]. However, previous studies [45,46] indicate that the electronic contribution to the thermal conductivity of graphene falls into the range of <1 W/mK to  $\sim 100$  W/mK depending on the carrier density and temperature, which is certainly much lower than the phonon contribution. Therefore, the electron transport can be described by the diffusive equations, as shown in Eq. (1) [47].

$$\frac{\partial}{\partial x} \left( k_{el} \frac{\partial T_{el}}{\partial x} \right) - S + \sum_{q,\nu} E_{eph}(x,q,\nu) = 0$$
(1)

where  $T_{el}$ , and  $k_{el}$  are the temperature, and effective thermal conductivity of electrons, respectively. The source term *S* includes the heat generation by Joule heating, and it also includes the interfacial energy exchange. The heat is then transferred to phonons via the electron-phonon energy exchange term,  $E_{eph}(x, q)$ , which is summed over all the phonon mode polarization *v* and wave vector *q* at a specific spatial cell *x*.

For solving the Boltzmann transport equation of phonons, various approximations for the phonon-phonon scattering term have been implemented with varying levels of accuracy. In the most complete form, this term should satisfy the conservation rules through the coupling of every phonon energy density to all the other phonons, and it has been implemented in several studies [5,20,48]. Application of such approach for simulating transport in a device structure can be computationally very expensive. The single mode relaxation time (SMRT) approximation uses the phonon relaxation time to represent the scattering of each phonon mode with all the others. Previous studies [5,48] show SMRT approach underestimates the thermal conductivity of pristine graphene, but still can capture the mode dependent phonon transport in a better

way than the gray and semi-gray BTE models. For graphene at high temperatures, the anharmonic scattering increases, and for graphene of nanometer size boundary scattering increases. The SMRT approach can provide a fairly good description in these conditions. In the present work, a hybrid multi-physics model involving graphene, substrate and metals, has been considered, so the implementation of the complete form of phonon BTE will be extremely time consuming. The SMRT approximation seems to be the best choice here considering the computational expense. For a phonon mode (*q*, *v*), the BTE can be written as [38,49]:

$$\nabla \cdot \left( \nu_{ph} \widehat{s} e_{ph} \right) - \frac{e_{ph}^{eq} - e_{ph}}{\tau_{ph}} - E_{eph} - S_{ph} = 0 \tag{2}$$

where  $e_{ph}$ ,  $v_{ph}$ , and  $\tau_{ph}$  are the energy density, group velocity, and lifetime of a phonon mode (q, v), respectively. The interfacial heat flux by phonons is included in the source term  $S_{ph}$ . In order to reduce the computation cost, 14 bands are considered for each phonon polarization (discretization for q). In each phonon mode, the energy flux is determined by integrating over its wave vector band. Equations (3) and (4) show expression for non-equilibrium energy flux and equilibrium energy flux, respectively for a phonon mode with wave-vector q and polarization v. The subscripts, j, k, and l, are the indices for the band of wave-vector q, the azimuthal angle  $\varphi$  and the polarization v, respectively.

$$e_{j,k,l}'' = \frac{1}{(2\pi)^2} \int_{q_{j-1/2,k}}^{q_{j+1/2,k}} \int_{\varphi_{l-1/2}}^{\varphi_{l+1/2}} e_{ph}(q, v_k) q d\varphi dq$$
(3)

$$e_{j,k}^{"eq} = \frac{\delta\varphi}{2\pi} \frac{1}{(2\pi)^2} \int_{q_{j-1/2,k}}^{q_{j+1/2,k}} e_{ph}^{eq}(q,v_k) 2\pi q dq$$
(4)

The relaxation time of each phonon mode is determined using the spectral energy density method, and the details can be found in our previous work [50]. Owing to the long MFP of phonons in graphene, the size effects due to the boundary scattering should be appropriately addressed in the phonon transport model. We have considered the size effects by including the boundary scattering in the calculation of total scattering rate of different phonon modes using the Matthiessen's rule [21]. Moreover, as demonstrated in the previous works [11,51,52], the coupling between graphene and substrate has impact on the phonon lifetime as well as the thermal conductivity. For the calculation of the phonon lifetime of graphene supported on a substrate, we have included scattering due to the substrate, and then determined the total scattering rates of different phonon modes using the Matthiessen's rule. The details of the calculations of substrate scattering can be found in Ref. [11]. The phonon lifetime of different modes are compared between the free standing graphene and SiO<sub>2</sub> supported graphene. As shown in Fig. S1 in the supplementary material, the substrate scattering has significant effects on the low frequency acoustic phonons.

The energy exchange rate  $E_{e-ph}(q, v)$  between electrons and phonons of (q, v) mode is determined using the coupling matrix  $g_{mn}^v(\mathbf{k}, \mathbf{q})$ , where *m* and *n* are the electronic band indices, *v* is the phonon polarization, and *k* and *q* are the wave vector for electron and phonon states, respectively [47,53,54].

$$E_{\rm e-ph}(q,v) = \frac{8\pi}{\hbar} \sum_{mn,\mathbf{k}} \hbar \omega_{\mathbf{q}v} |g_{mn}^{v}(\mathbf{k},\mathbf{q})|^{2} S(k,q) \delta \Big( \varepsilon_{m\mathbf{k}+\mathbf{q}} - \varepsilon_{n\mathbf{k}} - \hbar \omega_{\mathbf{q}v} \Big)$$
(5)

Here, the summation is over all electron bands mn and wave vectors k. S(k,q) is the thermal factor that considers the equilibrium distribution of electrons and phonons.

$$S(k,q) = N_q f_k \left( 1 - f_{k+q} \right) - \left( N_q + 1 \right) f_{k+q} (1 - f_k)$$
(6)

where  $f_k$  and  $N_q$  are the Fermi-Dirac distribution and Bose-Einstein distribution.

The energy exchange rate can be related to the imaginary part of the phonon self-energy  $\Pi_{av}^{''}$  arising from the EPI [55].

$$E_{e-ph}(q,v) = 8\omega_q \Big[ N\Big(\hbar\omega_q, T_{ph}\Big) - N\Big(\hbar\omega_q, T_e\Big) \Big] Im\Big[\Pi_{\mathbf{q}v}^{''}\Big]$$
(7)

$$\prod_{\mathbf{q}\nu}^{''} = \sum_{mn,\mathbf{k}} \left| g_{mn}^{\nu}(\mathbf{k},\mathbf{q}) \right|^2 \frac{f(\varepsilon_{n\mathbf{k}}) - f\left(\varepsilon_{m\mathbf{k}+\mathbf{q}}\right)}{\varepsilon_{n\mathbf{k}} - \varepsilon_{m\mathbf{k}+\mathbf{q}} - \hbar\omega_{q\nu} + i\eta}$$
(8)

The EPI matrix is determined using density functional perturbation theory which is implemented in Quantum-Espresso package. The phonon self-energy due to electron-phonon coupling is calculated using electron-phonon Wannier package [55,56] which employs maximally localized Wannier functions to interpolate the EPI matrix into a dense grid.

#### 2.2. Heat transfer model of substrate and metal contact

The thermal transport in amorphous  $SiO_2$  substrate is considered using a Fourier heat conduction equation.

$$\frac{\partial}{\partial x}\left(k\frac{\partial}{\partial x}T\right) + \frac{\partial}{\partial y}\left(k\frac{\partial}{\partial y}T\right) - S = 0$$
(9)

The electron and phonon thermal transport in metal is assumed to be diffusive, and a two-temperature model based on the diffusive heat conduction equations is employed.

$$\frac{\partial}{\partial x}\left(k_{e}\frac{\partial}{\partial x}T_{e}\right) + \frac{\partial}{\partial y}\left(k_{e}\frac{\partial}{\partial y}T_{e}\right) - \dot{Q}_{e-ph} + S = 0 \tag{10}$$

$$\frac{\partial}{\partial x}\left(k_{ph}\frac{\partial}{\partial x}T_{ph}\right) + \frac{\partial}{\partial y}\left(k_{ph}\frac{\partial}{\partial y}T_{ph}\right) + \dot{Q}_{e-ph} + S = 0$$
(11)

Here, the source term *S* includes the interfacial heat flux. The energy exchange rate between the electrons and phonons depend on the coupling constant and the electron and phonon temperature difference [47].

$$\dot{Q}_{e-ph} = G_{e-ph} \Big( T_e - T_{ph} \Big) \tag{12}$$

For materials with low Debye temperature, *e.g.*, metals,  $\hbar\omega_{\mathbf{q}v} < \langle k_B T_e$  and  $\hbar\omega_{\mathbf{q}v} < \langle k_B T_{ph}$ . Equation (6) can be rewritten as:

$$S(k,q) = \left(f_k - f_{k+q}\right) \frac{k_B \left(T_{ph} - T_e\right)}{\hbar \omega_{\mathbf{q}v}}$$
(13)

Substituting Eq. (13) into Eq. (5), one can obtain

In this case, a constant can be defined to describe the EPI which is independent on phonon distribution

$$G_{e-ph} = 4k_B \frac{2\pi}{\hbar} \sum_{mn,\mathbf{k},\nu,q} \left| g_{mn}^{\nu}(\mathbf{k},\mathbf{q}) \right|^2 \left( f_k - f_{k+q} \right) \delta \left( \varepsilon_{m\mathbf{k}+\mathbf{q}} - \varepsilon_{n\mathbf{k}} - \hbar \omega_{\mathbf{q}\nu} \right)$$
(15)

Finally, the electron-phonon energy exchange rate in Eq. (14) can be written in the form of Eq. (12) for metal contact.

#### 2.3. Interface coupling and boundary conditions

The interfaces considered in this model include the graphene/Cu and graphene/substrate interfaces. At graphene/Cu interfaces, both electrons and phonons can carry heat, and their heat flux is determined by Refs. [57,58].

$$J_e = \int_0^\infty \frac{1}{2\pi\hbar} (E - \mu) (f_F(E, T_1) - f_F(E, T_2)) \zeta_e(E) dE$$
(16)

$$J_{ph} = \int_{0}^{\infty} \frac{\hbar\omega}{2\pi} (f_B(\omega, T_1) - f_B(\omega, T_2)) \zeta_{ph}(\omega) d\omega$$
(17)

where  $\zeta_e$  and  $\zeta_{ph}$  are the transmission functions of electrons and phonons, respectively. The transmission functions are determined using Green's function method with DFT calculations [58,59].

At interfaces between graphene and  $SiO_2$  substrate, only phonons contribute to the interfacial heat flux. The  $SiO_2$  is in amorphous state, and the diffuse mismatch model [60] can be used to describe the phonon coupling at the interface between graphene and  $SiO_2$  substrate.

$$G = \frac{1}{4} \sum_{j}^{3} \int_{0}^{\omega_{\text{D}j}} \hbar \omega \nu_{1,j} D_1 \frac{\partial N}{\partial T} \alpha_{1-2,j}(\omega) d\omega$$
(18)

where  $\alpha_{1-2,j}$  is the transmission probability of phonon polarization *j* from material 1 (graphene) to material 2 (SiO<sub>2</sub>).  $D_1$  is the phonon DOS of material 1, and *N* is the Bose-Eisenstein distribution of phonons. The transmission probability can be evaluated using diffuse mismatch model

$$\alpha_{1-2j}(\omega) = \frac{\nu_{2j} D_{2j}(\omega)}{\nu_{1j} D_{1,j}(\omega) + \nu_{2j} D_{2j}(\omega)}$$
(19)

The effective DOS of graphene is evaluated using  $D_{eff}(\omega) = \omega/2\pi t v_{1,j}^2$  where the *c*-axis velocities of graphite are used for  $v_{1,j}$  [60]. The DOS of SiO<sub>2</sub> is evaluated using a sine dispersion curves  $\omega_j = \omega_{D,j} \sin(\pi k/2k_{D,j})$  with a cutoff wave vector determined by Debye temperature  $k_{D,j} = k_B \Theta_D / \hbar v_{2,j}$  [18]. The TBC at SLG/SiO<sub>2</sub> and SLG/Cu interfaces are predicted and compared with the available experimental data in literature, and detailed discussion can be found in the supplementary document.

In Fig. 1, boundary conditions for SLG, Cu contacts, and SiO<sub>2</sub>

$$E_{\mathrm{e-ph}} = \left[ 4k_B \frac{2\pi}{\hbar} \sum_{mn,\mathbf{k},\nu,q} \left| g_{mn}^{\nu}(\mathbf{k},\mathbf{q}) \right|^2 \left( f_k - f_{k+q} \right) \delta\left( \varepsilon_{m\mathbf{k}+\mathbf{q}} - \varepsilon_{n\mathbf{k}} - \hbar \omega_{\mathbf{q}\nu} \right) \right] \left( T_{ph} - T_e \right)$$
(14)

substrate have been specified. Since the thickness of SLG is very small (0.335 nm), the heat flux at SLG edges is neglected, and the reflecting boundary condition with assumption of specular reflection is applied for the phonons escaping the domain. For solving Fourier heat conduction equations for Cu contact and SiO<sub>2</sub> substrate, constant temperature (300 K) boundary conditions are applied. The source term in phonon BTE equation is specified by the electron-phonon energy exchange, and the heat exchange by both electrons and phonons are considered at the interfaces. The electron diffusion equation (1) and phonon BTE equation (2) are discretized using the finite volume method, and the nonlinear equations are solved using Newton-Raphson method. The flow chart of the numerical implementation of the governing equations is shown in Fig. 2. The convergence criterion is set at  $10^{-5}$  for the temperature residual. The difference between the heating power and the total heat transfer rate is also examined for the convergence. The total heat transfer rate includes the heat transferred through the SLG/SiO<sub>2</sub> interface by phonons and through the SLG/Cu interface by both electrons and phonons.

#### 3. Results and discussion

#### 3.1. Electron phonon interaction strength in graphene

The phonon self-energy  $\Pi''_{qv}$  for EPI can be determined using Eq. (8) using DFT calculations. The imaginary part of  $\Pi''_{qv}$  is a measure of the electron-phonon scattering rate. The polarization and wave-vector dependent imaginary parts of  $\Pi''_{qv}$  are calculated at different electron temperatures (100 K–3000 K) which are used in Eq. (7) for the calculation of energy exchange rates between electrons and phonons. The DFT calculations to estimate  $\Pi''_{qv}$  are performed at electron temperature ranging from 100 K to 3000 K with an interval of 100 K. Linear interpolation is employed to determine  $\Pi''_{qv}$  at an arbitrary electron temperature during the iterative solution of electron/phonon temperature using hybrid BTE model. The imaginary parts of  $\Pi''_{qv}$  are plotted and discussed for different phonon modes in the first Brillouin zone (FBZ) in Fig. 3. Since the

EPI strength is nearly zero for most wave vectors in the FBZ except for the region near  $\Gamma$ , K and K' points, the contour plots in these figures only show the region within a radius of  $\delta q/q_0 < 0.1$ , where  $\delta q = q - q_i$  with  $j = \Gamma$ , K, or K' and  $q_0 = 2\pi/a$ .

As shown in Fig. 3, the self-energy is non-zero for LO, TO and LA modes, while the self-energy of TA, ZA, and ZO phonons is negligible. Fig. 3 (a) and (b) shows that the interaction between LO phonons and electrons is focused in a small region around  $\Gamma$  point. As shown in Fig. 3 (c) and (d), the self-energy of TO phonons is noticeable in the region around  $\Gamma$  point, and it also has small values in the regions near K and K'. Comparing with Fig. 3 (a) and (b), the peak values of the imaginary part of  $\Pi''_{\mathbf{q}\nu}$  for TO modes are about 3 times larger than that of LO modes. Fig. 3 (e) and (f) indicates that the self-energy of LA phonons is one order of magnitude smaller than that of LO and TO phonons, and it is non-negligible only around K and K' points. As the temperature increases from 300 K to 1500 K, the magnitude of self-energy becomes larger, and the regime with non-zero values in FBZ also becomes larger for each of the LO, TO, and LA phonons.

### 3.2. Heat dissipation in suspended SLG without Cu contact (system I)

Fig. 4 shows the temperature profiles for electrons and phonons along the SLG with different length. In the SLG, electrons can only transfer heat to phonons. If the SLG length *L* is small, electrons and phonons are in high non-equilibrium: electron temperature is high while phonon temperature is low. Since the thermal resistance along the SLG is small, the electron and phonon temperatures are quite uniform in SLG at small *L*, *e.g.*, for *L*=24.5 nm as shown in Fig. 4 (a) and (b). As *L* increases, electrons and phonons tend to equilibrate with each other: electron temperature drops while phonon temperature at the center of SLG (x = 0) changes little after *L* >73.5 nm. This is because electrons receive heat only around the SLG center and lose the heat to phonons along the entire SLG. It can be an analogy to the heat transfer in a one-dimension fin, and further increases of *L* 



Fig. 2. Flow chart showing the implementation of the hybrid model. (A colour version of this figure can be viewed online.)



**Fig. 3.** Imaginary part of phonon self-energy in the first Brillouin zone with different electron temperatures: (a) LO mode at 300 K; (b) LO mode at 1500 K; (c) TO mode at 300 K; (d) TO mode at 300 K; (e) LA mode at 300 K; (f) LA mode at 1500 K. As denoted in (a), *Γ* point is the zone center while *K* point is the corner of the first Brillouin zone. (A colour version of this figure can be viewed online.)



**Fig. 4.** (a) Electron and (b) phonon temperature profiles along SLG with different length in System I: SLG suspended on SiO<sub>2</sub> as shown in Fig. 1 (a). The *x*-axis is normalized with their SLG length, while the legend denotes different SLG length. The heating power is 0.04 mW. (A colour version of this figure can be viewed online.)

beyond certain values cannot help to reduce the temperature in the center.

On the contrary, the phonon temperature along the SLG increases as *L* increases from 24.5 nm to 245 nm as shown in Fig. 4 (b). It can be also observed that the phonon temperature at x = 0 increases faster as *L* increases. This is because phonons receive heat from electrons along the entire SLG but can only transfer heat near its edge contacting with SiO<sub>2</sub>. The thermal resistance of SLG for heat dissipation increases with the SLG length, which leads to a higher phonon temperature in SLG as shown in Fig. 4 (b).

#### 3.3. Heat dissipation in suspended SLG with Cu contact (system II)

Fig. 5 (a) and (b) shows the temperature profiles along the SLG in System II for electrons and phonons, respectively. The suspended SLG in System II can dissipate heat to both Cu and SiO<sub>2</sub> near the edge, *i.e.*,  $x/L = \pm 1$ . At a small *L*, electrons and phonons in the SLG

are at a highly non-equilibrium state: the electron temperature is high while phonon temperature is low. The heat absorbed by electrons is mainly dissipated to Cu contacts directly, and only a small portion is transferred to phonons in SLG. The heat dissipation at the SLG/SiO<sub>2</sub> interface is inefficient due to the low phonon temperature. Therefore, the SLG/Cu interface is the major heat dissipation pathway for the SLG with a small *L* as shown in Fig. 6 (a) and (b).

As *L* increases, electrons and phonons equilibrates, and more heat is transferred to phonons in the SLG. At a larger *L*, electron temperature becomes smaller and phonon temperature increases near the contact region with  $SiO_2$  and Cu, as shown in Fig. 5 (b). Therefore, the heat dissipation at SLG/Cu interfaces decreases while the heat dissipation at SLG/SiO<sub>2</sub> interfaces increases, as shown in Fig. 6 (a) and (b).

It can be observed from Fig. 6 (b) that the heat dissipation at SLG/Cu interfaces is larger than that at  $SLG/SiO_2$  interfaces as



**Fig. 5.** (a) Electron and (b) phonon temperature profiles along SLG with different length in System II: SLG suspended on SiO<sub>2</sub> with Cu contacts as shown in Fig. 1 (b). The *x*-axis is normalized with their SLG length, while the legend denotes different SLG length. The heating power is 0.04 mW. (A colour version of this figure can be viewed online.)



**Fig. 6.** Heat dissipation pathways in Structure II: SLG suspended on SiO<sub>2</sub> with Cu contacts as shown in Fig. 1 (b). (a) Heat transfer rate via SiO<sub>2</sub> and Cu as a function of SLG length. The arrows show the direction of increasing heating power from 0.02 mW to 0.1 mW with an interval of 0.02 mW. (b) Ratio of heat transfer rate via Cu contacts to total heating power as a function of SLG length. The legend denotes different heating power. (A colour version of this figure can be viewed online.)

indicated by the  $Q_m/Q_{total} \approx 0.65$  for large *L*. Since the contact length of SLG with Cu and SiO<sub>2</sub> are same in System II, the heat dissipation at SLG/Cu and SLG/SiO<sub>2</sub> interfaces are determined by their TBC and temperature difference. It is worth noting that the TBC at SLG/SiO<sub>2</sub> interface is larger than that at SLG/Cu interface as demonstrated by recent measurements [15,61] and numerical predictions [50,62,63]. The predictions of TBC in present study are shown in Fig. S1 of the supplementary material. The temperature difference at SLG/Cu interfaces should be large than that at SLG/SiO<sub>2</sub> interfaces. This is confirmed by comparing the maximum



**Fig. 7.** Variations of maximum temperature in (a) Cu contact and (b) SiO<sub>2</sub> contact as a function of SLG length in Structure II: SLG suspended on SiO<sub>2</sub> with Cu contacts as shown in Fig. 1 (b). The arrows show the direction of increasing heating power from 0.02 mW to 0.1 mW with an interval of 0.02 mW. (A colour version of this figure can be viewed online.)

temperature in Cu and SiO<sub>2</sub> contacts in Fig. 7 (a) and (b) with the SLG temperature near contact in Fig. 5 (b). In Fig. 7, *L* denotes the SLG length while the arrows show the direction of increasing heating power from 0.02 mW to 0.1 mW with an interval of 0.02 mW. The maximum temperature in SiO<sub>2</sub> is well above the boundary temperature of 300 K at a large SLG length *L* while the maximum temperature in Cu is close to 300 K for all the heating power and SLG length *L* considered. This is because the low thermal conductivity of SiO<sub>2</sub> deteriorates the heat conduction and leads to a large thermal resistance as well as a large temperature gradient in SiO<sub>2</sub>. Therefore, SLG/Cu interface can still be an effective heat dissipation pathway if its contact area is comparable to that of SLG/SiO<sub>2</sub> interfaces, even though it has lower TBC than SLG/SiO<sub>2</sub> interface.

## 3.4. Heat dissipation in supported SLG without Cu contact (system III)

In System III, the SLG is fully supported on SiO<sub>2</sub> substrate as shown in Fig. 1 (c). The heat absorbed by electrons can only be transferred to phonons in SLG, followed by the heat dissipation to the SiO<sub>2</sub> substrate via phonons. As shown in Fig. 8 (a) and (b), both electron and phonon temperatures decrease as the SLG length increases for System III. This is comprehensible because the heating power is fixed while the heat transfer area between SLG and SiO<sub>2</sub> substrate increases as the SLG length increases. This trend is larger for the SLG with a small length, *e.g.*, from 24.5 nm to 73.5 nm.

Moreover, the temperature distribution is nearly uniform in SLG



**Fig. 8.** (a) Electron and (b) phonon temperature profiles along SLG with different length in System III: SLG supported on SiO<sub>2</sub> substrate as shown in Fig. 1 (c). The *x*-axis is normalized with their SLG length; the legend denotes different SLG length. The heating power is 0.04 mW. (A colour version of this figure can be viewed online.)

with a small length, *e.g.*, *L*=24.5 nm in Fig. 8 (a) and (b). This is because heat can spread efficiently in the small SLG due to the high thermal conductivity. Meanwhile, electrons and phonons are in high non-equilibrium in the SLG of a small length. Therefore, the heat dissipation in the small length SLG behaves like the heat transfer between heat reservoirs with thermal resistances in series: the thermal resistance  $R_{e-ph}$  for the electron-phonon heat exchange, and the contact resistance  $R_{subs}$  between SLG and SiO<sub>2</sub> substrate. As the SLG length increases, the temperature drop along SLG increases for both electrons and phonons. The electrons and phonons can approximately reach thermal equilibrium state in the region far away from the heating zone at the center for a large length SLG, *e.g.*, *L*=490 nm.

#### 3.5. Heat dissipation in supported SLG with Cu contact (system IV)

Compared with the SLG in System III, the SLG in System IV has contacts with Cu in addition to the contact with  $SiO_2$  substrate, as shown in Fig. 1 (d). The electrons in SLG can transfer heat to phonons in SLG as well as the electrons in Cu contacts through the SLG/Cu interfaces. This pathway via Cu contacts can have significant impact on the heat dissipation in the SLG of short length.

Comparing the electron temperature in Fig. 8 (a) and Fig. 9 (a), it can be observed that the electron temperature is lower in SLG with L of 24.5 nm and 73.5 nm. Similar trend can be observed for phonon temperature in Fig. 8 (b) and Fig. 9 (b) for SLG with L of 24.5 nm and 73.5 nm. However, the magnitude and distribution of both electron

and phonon temperature remain similar as the SLG in System III of large *L*, *e.g.*, 245 nm and 490 nm, no matter whether the SLG has Cu contacts or not. This is because  $SLG/SiO_2$  interfaces become the major heat transfer pathway for the SLG with a large length. In this situation, most of the heat is dissipated into  $SiO_2$  substrate via the electron-phonon-SiO<sub>2</sub> path, and then electrons reach thermal equilibrium with phonons near the edges of SLG.

Fig. 10 (a) shows the variations of heat transfer rate via SiO<sub>2</sub> substrate and Cu contacts as a function of SLG length in System IV for different heating power. As shown in Fig. 10 (a), the heat dissipation at SLG/Cu interfaces dominates at a small SLG length, but decreases as the SLG length increases, and finally becomes negligible for the large SLG length above 490 nm. The heat dissipation at SLG/SiO<sub>2</sub> interfaces has an opposite trend when SLG length increases. Fig. 10 (b) shows the ratio of heat transfer rate  $Q_m$ at the SLG/Cu interface to the total heat transfer rate Q<sub>total</sub>. It clearly shows that the major heat dissipation pathway is switched from the SLG/Cu interface to the SLG/SiO<sub>2</sub> interface as the SLG length increases for all heating power considered. It can be also observed, in Fig. 10 (b), that  $Q_m/Q_{total}$  decreases with increasing heating power for a constant SLG length. This is because the electron-phonon heat exchange is enhanced at higher temperature due to the increased EPI strength as shown in Fig. 3.

#### 3.6. Discussion of the heat transfer pathways in SLG nanostructures

The maximum temperature in SLG is an important parameter to



**Fig. 9.** (a) Electron and (b) phonon temperature profiles along SLG with different length in System IV: SLG supported on SiO<sub>2</sub> substrate with Cu contacts as shown in Fig. 1 (d). The x-axis is normalized with their SLG length; the legend denotes different SLG length. The heating power is 0.04 mW. (A colour version of this figure can be viewed online.)



**Fig. 10.** Heat dissipation pathways in Structure IV: SLG supported on SiO<sub>2</sub> with Cu contacts as shown in Fig. 1 (d). (a) Heat transfer rate via SiO<sub>2</sub> and Cu as a function of SLG length. The arrows show the direction of increasing heating power from 0.02 mW to 0.1 mW with an interval of 0.02 mW. (b) Ratio of heat transfer rate via Cu contacts to the total heating power as a function of SLG length. The legend denotes different heating power. (A colour version of this figure can be viewed online.)



Fig. 11. Electron and phonon temperatures at the center of SLG with different lengths for (a) System I and (b) System II. The arrows show the direction of increasing heating power from 0.02 mW to 0.1 mW with an interval of 0.02 mW. (A colour version of this figure can be viewed online.)

consider for both efficient thermal management and improving reliability of the SLG electronic devices. The maximum temperature occurs at the center of SLG for all systems as expected. The peak temperature,  $T_{max}$ , is shown in Fig. 11 (a) and (b) as a function of SLG length at different heating power for System I and II, respectively. Due to the electron-phonon non-equilibrium, electron temperature is high while phonon temperature is low at x = 0 in the SLG of a small *L* in System I, as shown in Fig. 11 (a). It can be also seen that the maximum electron temperature (at x = 0) increases slowly as *L* increases beyond a certain value, *e.g.*, 73.5 nm for I > 0.08 mW. In System II, the SLG/Cu interface is an important heat dissipation pathway for both electrons and phonons at a small *L*. So as *L* increases, the maximum electron and phonon temperatures increase

faster when L is small, and then increase with a smaller rate, as shown in Fig. 11 (b).

In System III and IV, the peak temperature is shown in Fig. 12 (a) and (b) as a function of SLG length at different heating power. In System III, the peak temperatures of electrons and phonons drop quickly as the SLG length increases, and then remain nearly constant after L > 100 nm. This is simply because the SLG/SiO<sub>2</sub> interface is the only heat dissipation pathway and the interface area increases with the SLG length.

However, the SLG/Cu interface is the major heat dissipation pathway when SLG length is small in System IV. The thermal energy is mainly stored in electrons due to the high non-equilibrium between electrons and phonons. As the SLG length increases, the



Fig. 12. Electron and phonon temperatures at the center of SLG with different length for (a) System III and (b) System IV. The arrows show the direction of increasing heating power from 0.02 mW to 0.1 mW with an interval of 0.02 mW. (A colour version of this figure can be viewed online.)

heat dissipation at SLG/Cu interfaces decreases rapidly as shown in Fig. 10, and the temperatures of electrons and phonons at SLG/Cu interfaces also decrease. The increase of resistance from center to SLG/Cu contact cannot be compensated by the decrease in resistance due to increased SLG/SiO<sub>2</sub> contact length. As a result, the peak temperatures in System IV increase with SLG length when L < 100 nm. At a large SLG length, the heat dissipation at the SLG/Cu interface becomes negligible (see Fig. 10), and therefore the peak temperature saturates as shown in Fig. 12 (b).

#### 4. Conclusions

In summary, the electron-phonon coupled thermal transport is modeled in the suspended or supported SLG with/without Cu contacts. Using first-principle calculations, the EPI strength of different phonon modes are obtained at different temperatures. The *k*-space distribution of EPI strength shows that the EPI is strong for LO and TO modes, moderate for LA modes, and negligible for TA, ZA, and ZO modes. The EPI strength increases with temperature, which indicates the enhanced electron-phonon heat exchange at high temperature.

The maximum temperature in SLG and the role of SLG/Cu and SLG/SiO<sub>2</sub> interfaces in the heat dissipation depend on both the SLG support configuration and the SLG length. The SLG/Cu interface is an important heat dissipation pathway for both electrons and phonons in the suspended SLG with Cu contact, and it can significantly improve the heat dissipation and lower the SLG temperature compared with that of SLG suspended on SiO<sub>2</sub> without Cu contact. The SLG/Cu interface can be the major heat dissipation pathway when its contact area is comparable to that of SLG/SiO<sub>2</sub> interface, which can be attributed to the higher thermal conductivity of Cu. The presence of Cu contact can be effective in lowering the temperature in supported SLG on SiO<sub>2</sub> only when SLG length is small. When the SLG length is large in the supported SLG on SiO<sub>2</sub> with Cu contact at ends, the SLG/SiO<sub>2</sub> interface becomes the major heat dissipation pathway while the heat transfer at the SLG/Cu interface becomes negligible. Due to the increased EPI strength in graphene at a higher temperature, the electron-phonon heat exchange is enhanced, and the percentage of heat dissipated through the Cu contact decreases at a high heating power. The findings in this work can provide critical insights into the electron-phonon coupled thermal transport in graphene nanostructures, and benefit the efficient thermal management of graphene nano-electronics.

#### Appendix A. Supplementary data

Supplementary data related to this article can be found at http://dx.doi.org/10.1016/j.carbon.2017.07.095.

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